

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	3709	((stor\$4 or sav\$4 or writ\$4)with ((primary or first or one or correspond\$4)near2 (memory or storage or fifo)))same ((primary or first or one or correspond\$4)near2 clock)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:46
L7	3	L6 and (((adjust\$4 or control\$4 or alter\$5 or manag\$4 or shift\$4 or chang\$4)near2 (clock or phase))with ((occupancy or fullness)near2 (buffer or memory or storage)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:46
L8	677	((adjust\$4 or control\$4 or (phase adj shift\$4) or delay\$4 or manag\$4 or alter\$5)near2 (clock or speed or rate or frequenc\$3 or writ\$4))with ((align\$5 or synchroniz\$5 or adjust\$4 or maintain\$4 or manag\$4 or control\$4)near3 ((occupanc\$3 or full\$4 or level)near2 (buffer or fifo or memory or storgae)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:53
L9	41	L8 with (((first or transmi\$5 or receiv\$4 or primary) near2 (buffer or fifo or memory or storage)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:53
L10	41072	"345"/\$.ccls.	USPAT	OR	ON	2007/03/05 09:58
L11	0	I10 and (((stor\$4 or sav\$4 or writ\$4)near5 ((primary or first or transmit\$4)near2 (memory or fifo or register or storage)))same ((second\$4 or other or receiv\$4)near2 (memory or fifo or register or storage)))same (((adjust\$4 or shift\$4 or delay\$4)near3 (clock or frequency))with occupancy))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:58
L12	20793	"710"/\$.ccls.	USPAT	OR	ON	2007/03/05 09:58

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L13	0	I12 and (((stor\$4 or sav\$4 or writ\$4)near5 ((primary or first or transmit\$4)near2 (memory or fifo or register or storage))same ((second\$4 or other or receiv\$4)near2 (memory or fifo or register or storage)))same (((adjust\$4 or shift\$4 or delay\$4)near3 (clock or frequency))with occupancy))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:58
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**PALM INTRANET**

Day : Monday  
 Date: 3/5/2007  
 Time: 10:10:32

**Inventor Name Search Result**

Your Search was:

Last Name = MCCLARY

First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09835474</a>	6941381	150	04/16/2001	METHOD AND APPARATUS FOR SYNC HUNTING SIGNALS	MCCLARY, MICHAEL
<a href="#">09835704</a>	Not Issued	66	04/16/2001	Method and apparatus for deframing signals	MCCLARY, MICHAEL
<a href="#">09835979</a>	6950446	150	04/16/2001	METHOD AND APPARATUS FOR SIMULTANEOUSLY SYNC HUNTING SIGNALS	MCCLARY, MICHAEL
<a href="#">09836950</a>	6510166	150	04/17/2001	STUFFING FILTER MECHANISM FOR DATA TRANSMISSION SIGNALS	MCCLARY, MICHAEL
<a href="#">09837448</a>	6959008	150	04/17/2001	ALIGNMENT OF TDM-BASED SIGNALS FOR PACKET TRANSMISSION USING FRAMED AND UNFRAMED OPERATIONS	MCCLARY, MICHAEL
<a href="#">10014726</a>	Not Issued	41	12/10/2001	Method and apparatus for using timeslots within a signal	MCCLARY, MICHAEL
<a href="#">10080869</a>	Not Issued	71	02/21/2002	Method and apparatus for balancing bandwidth among multiple ports of a network element	MCCLARY, MICHAEL
<a href="#">10102461</a>	7075951	150	03/20/2002	METHOD AND APPARATUS FOR THE OPERATION OF A STORAGE UNIT IN A NETWORK ELEMENT	MCCLARY, MICHAEL
<a href="#">10313703</a>	Not Issued	41	12/06/2002	Method and apparatus for altering an output rate of a data storage unit	MCCLARY, MICHAEL
<a href="#">10388914</a>	Not Issued	60	03/13/2003	Network element having a cross-connect to route data to traffic cards of varying functionality	MCCLARY, MICHAEL
<a href="#">10602461</a>	Not Issued	71	06/23/2003	Repeating switching of a cross-connect and a timing source in a network element through the use of a phase adjuster	MCCLARY, MICHAEL
<a href="#">10602481</a>	Not Issued	41	06/23/2003	Switching of a cross-connect and a timing source in a network element	MCCLARY, MICHAEL
<a href="#">60280661</a>	Not Issued	159	03/31/2001	Alignment fo TDM-based signals for packet transmission using framed and unframed operations	MCCLARY, MICHAEL
<a href="#">60280662</a>	Not Issued	159	03/31/2001	Stuffing filter mechanism for data transmission signals	MCCLARY, MICHAEL

<a href="#"><u>60280694</u></a>	Not Issued	159	03/31/2001	Method and apparatus for processing multiple communications signals in one clock domain	MCCLARY, MICHAEL
<a href="#"><u>60334486</u></a>	Not Issued	159	11/29/2001	Method and apparatus for the operation of a storage unit in a network element	MCCLARY, MICHAEL
<a href="#"><u>60340925</u></a>	Not Issued	159	12/07/2001	Method and apparatus for altering an output rate of a data storage unit	MCCLARY, MICHAEL
<a href="#"><u>60340982</u></a>	Not Issued	159	12/07/2001	Balancing bandwidth among multiple ports of a network element	MCCLARY, MICHAEL
<a href="#"><u>60363967</u></a>	Not Issued	159	03/14/2002	Network element having a cross-connect to route data to traffic cards of varying functionality	MCCLARY, MICHAEL
<a href="#"><u>60367233</u></a>	Not Issued	159	03/25/2002	Network element having a cross-connect to route data to traffic cards of varying functionality	MCCLARY, MICHAEL
<a href="#"><u>60390630</u></a>	Not Issued	159	06/21/2002	Switching of a cross-connect and a timing source in a network element	MCCLARY, MICHAEL

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>
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 Time: 10:10:45

**Inventor Name Search Result**

Your Search was:

Last Name = NARAHARI

First Name = SHARATH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08477021</a>	Not Issued	166	06/07/1995	METHOD OF CONSTRUCTING A PLUG AND PLAY COMPATIBLE BUS CARD WHICH ALLOWS FOR MASS PRODUCTION OF THE BUS CARD	NARAHARI, SHARATH
<a href="#">09835474</a>	6941381	150	04/16/2001	METHOD AND APPARATUS FOR SYNC HUNTING SIGNALS	NARAHARI, SHARATH
<a href="#">09835704</a>	Not Issued	66	04/16/2001	Method and apparatus for deframing signals	NARAHARI, SHARATH
<a href="#">09835979</a>	6950446	150	04/16/2001	METHOD AND APPARATUS FOR SIMULTANEOUSLY SYNC HUNTING SIGNALS	NARAHARI, SHARATH
<a href="#">09837448</a>	6959008	150	04/17/2001	ALIGNMENT OF TDM-BASED SIGNALS FOR PACKET TRANSMISSION USING FRAMED AND UNFRAMED OPERATIONS	NARAHARI, SHARATH
<a href="#">10014726</a>	Not Issued	41	12/10/2001	Method and apparatus for using timeslots within a signal	NARAHARI, SHARATH
<a href="#">10109521</a>	Not Issued	41	03/28/2002	Method and apparatus for processing channelized and unchannelized data within a signal	NARAHARI, SHARATH
<a href="#">10388914</a>	Not Issued	60	03/13/2003	Network element having a cross-connect to route data to traffic cards of varying functionality	NARAHARI, SHARATH
<a href="#">10602461</a>	Not Issued	71	06/23/2003	Repeating switching of a cross-connect and a timing source in a network element through the use of a phase adjuster	NARAHARI, SHARATH
<a href="#">10602481</a>	Not Issued	41	06/23/2003	Switching of a cross-connect and a timing source in a network element	NARAHARI, SHARATH
<a href="#">60333845</a>	Not Issued	159	11/28/2001	Method and apparatus for processing channelized and unchannelized data within a signal	NARAHARI, SHARATH
<a href="#">60363967</a>	Not Issued	159	03/14/2002	Network element having a cross-connect to route data to traffic cards of varying functionality	NARAHARI, SHARATH
<a href="#">60367233</a>	Not Issued	159	03/25/2002	Network element having a cross-connect to route data to traffic cards of varying functionality	NARAHARI, SHARATH

60390630	Not Issued	159	06/21/2002	Switching of a cross-connect and a timing source in a network element	NARAHARI, SHARATH
08877207	5892972	150	06/17/1997	METHOD OF CONSTRUCTING A PLUG AND PLAY COMPATIBLE BUS CARD WHICH ALLOWS FOR MASS PRODUCTION OF THE BUS CARD	NARAHARI, SHARATH C.

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

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1 Scheduling I/O transfers in a 2-D mesh with packet deadlines



S. R. Subramanya, Bhagirath Narahari

April 1998 **Proceedings of the 36th annual Southeast regional conference ACM-SE**

36

Publisher: ACM Press

Full text available: pdf(341.38 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

2 Efficient algorithms for erasure node placement on slotted dual bus networks



Bhagirath Narahari, Sunil Shende, Rahul Simha

October 1996 **IEEE/ACM Transactions on Networking (TON)**, Volume 4 Issue 5

Publisher: IEEE Press

Full text available: pdf(645.61 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

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